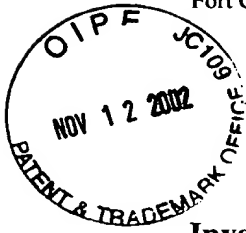


HEWLETT-PACKARD COMPANY  
Intellectual Property Administration  
P.O. Box 272400  
Fort Collins, Colorado 80527-2400

#12/B  
PATENT HE  
11-15-02  
P. Hull



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Inventor** Fong PONG **Examiner:** Jasmine Song  
**Serial No.:** 09/444,173 **Confirmation No.:** 8306  
**Filed:** Nov. 19, 1999 **Group Art Unit:** 2187

**Title:** Asynchronous Cache Coherence Architecture In A Shared Memory Multiprocessor  
With Point-to-Point Links

OK to enter  
3/7/03  
Commissioner for Patents  
Washington, D.C. 20231

RECEIVED  
NOV 15 2002  
Technology Center 2100

AMENDMENT/REPLY UNDER 37 C.F.R. §1.116

Sir:

In reply to the Final Office Action mailed September 11, 2002 (the "Office Action"),  
please amend the application as follows, and consider the following remarks.

IN THE CLAIMS

*Please amend the following claims.*

- Sub D1  
B1
1. (Once Amended) A method for accessing memory in a multiprocessor system,  
comprising:  
from a requesting processor, issuing a request for data to one or more other processors  
and memory, each copy of the data being associated with a state information indicating  
whether the copy is valid or not;  
in each of the processors and memory that receive the request, checking to determine  
whether a valid copy of the data exists; and